

ON THE DECODING FAILURE RATE OF QC-MDPC BIT-FLIPPING DECODERS

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- McEliece-like public-key encryption scheme with a quasi-cyclic structure
 - Reasonable key sizes
 - Reduction to generic hard problems over quasi-cyclic codes
- 2nd round candidate to the NIST post-quantum cryptography standardization process
 - BIKE

¹Rafael Misoczki et al. 'MDPC-McEliece: New McEliece variants from Moderate Density Parity-Check codes'. In: *Proc. IEEE Int. Symposium Inf. Theory - ISIT.* 2013, pp. 2069–2073.

ANALYSIS OF THE DECODER

Methodology:

- Prove that the Decoding Failure Rate is negligible in an ideal model
- Study the validity of the model

Motivations:

- Security reasons
 - [GJS16]²: correlation between faulty error patterns and the secret key
→ Scheme is not IND-CCA
- Engineering reasons
 - Avoid re-execution of the protocol in case of failure
 - Misuse resilience

²Qian Guo, Thomas Johansson and Paul Stankovski. 'A Key Recovery Attack on MDPC with CCA Security Using Decoding Errors'. In: *Advances in Cryptology - ASIACRYPT 2016*. Ed. by Jung Hee Cheon and Tsuyoshi Takagi. Vol. 10031. LNCS. 2016, pp. 789–815. ISBN: 978-3-662-53886-9. DOI: [10.1007/978-3-662-53887-6_29](https://doi.org/10.1007/978-3-662-53887-6_29). URL: http://dx.doi.org/10.1007/978-3-662-53887-6_29.

DECODING ALGORITHM (*BIT-FLIPPING*)

Original

Input

$$H \in \{0, 1\}^{r \times n}$$

$$y \in \{0, 1\}^n$$

H: moderately sparse parity check matrix

$$y = c + e$$

Output

$$c \in \{0, 1\}^n$$

y: noisy codeword

c: codeword

e: error

while $yH^T \neq 0$ **do**

$$s \leftarrow yH^T$$

$$s = yH^T = \underbrace{cH^T}_{=0} + eH^T$$

s: syndrome

$T \leftarrow \text{threshold(context)}$

for $j \in \{0, \dots, n-1\}$ **do**

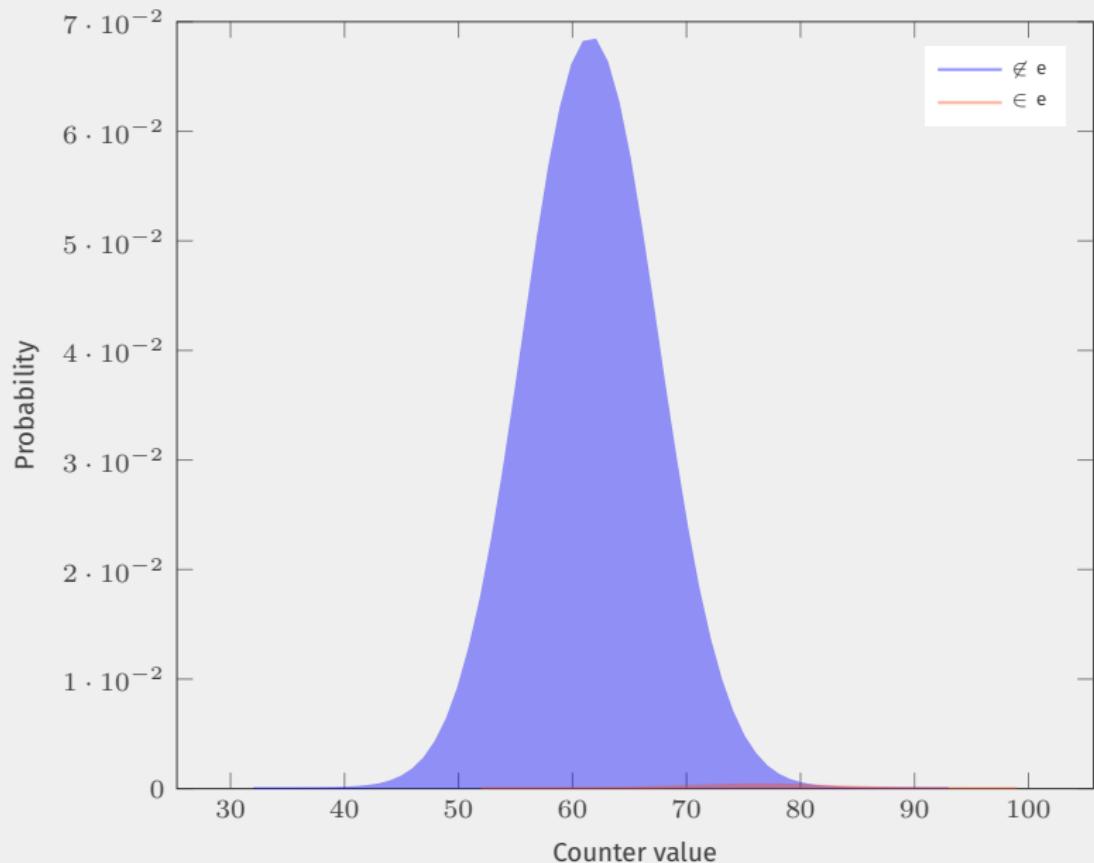
if $|s \cap h_j| \geq T$ **then**

$$y_j \leftarrow 1 - y_j$$

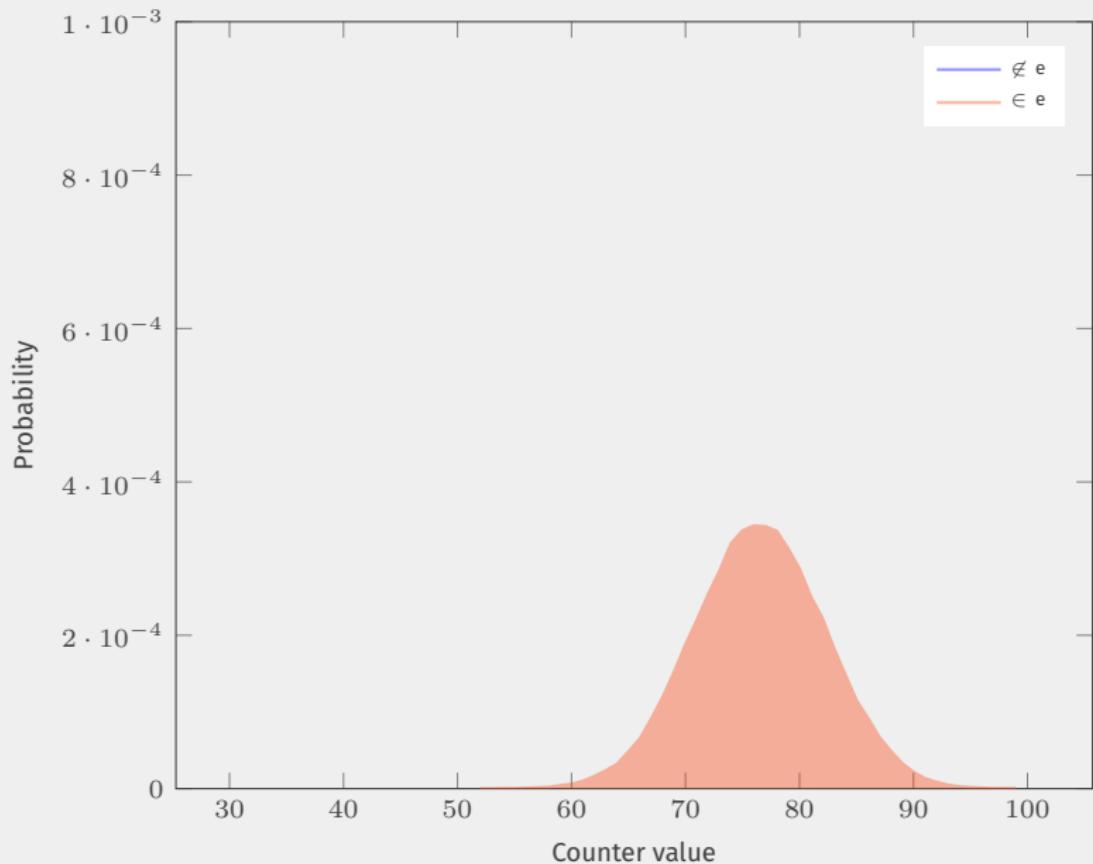
$|s \cap h_j|$: counter

return y

COUNTERS DISTRIBUTIONS: $|S| = 14\,608$, $|E| = 264$



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Original

Input

$$\begin{aligned} H &\in \{0, 1\}^{r \times n} \\ y &\in \{0, 1\}^n \end{aligned}$$

Output

```
c ∈ {0, 1}^n
while yH^T ≠ 0 do
    s ← yH^T
    T ← threshold(context)
    for j ∈ {0, ..., n - 1} do
        if |s ∩ h_j| ≥ T then
            y_j ← 1 - y_j
return y
```

Step-by-step

Input

$$\begin{aligned} H &\in \{0, 1\}^{r \times n} \\ y &\in \{0, 1\}^n \end{aligned}$$

Output

```
c ∈ {0, 1}^n
while yH^T ≠ 0 do
    s ← yH^T
    j ← sample(context)
    T ← threshold(context)
    if |s ∩ h_j| ≥ T then
        y_j ← 1 - y_j
return y
```

MODEL FOR A DECODER

- Finite State Machine
- Stochastic process
- Suppose it is a memoryless process
→ Markov chain

State space:

- all the possible combinations of (S, t) with
 - $S = |\mathbf{eH}^T|$: the syndrome weight
 - $t = |\mathbf{e}|$: the error weight

Transitions:

- Defined by the algorithm

For a specific starting syndrome weight $|s| = S$ and error weight $|\mathbf{e}| = t$:

$$P_{\text{success}}(S, t) = \Pr[(S, t) \xrightarrow{\infty} (0, 0)] \quad P_{\text{failure}}(S, t) = 1 - P_{\text{success}}(S, t)$$

Finally

$$\text{DFR}(t) = \sum_S \Pr(|s| = S \mid |\mathbf{e}| = t) \cdot P_{\text{failure}}(S, t)$$

ASSUMPTIONS

- Error positions are always independent

- Infinite number of iterations

- Counters distributions [Cha17]³:

- $\Pr [|s \cap h_j| = \sigma |e_j = 0|] = \binom{d}{\sigma} \pi_0^\sigma (1 - \pi_0)^{d-\sigma}$ with

$$\pi_0 = \frac{\bar{\sigma}_{\text{corr}}}{d} = \frac{(w-1)|s| - X}{d(n-|e|)}$$

- $\Pr [|s \cap h_j| = \sigma |e_j = 1|] = \binom{d}{\sigma} \pi_1^\sigma (1 - \pi_1)^{d-\sigma}$ with

$$\pi_1 = \frac{\bar{\sigma}_{\text{err}}}{d} = \frac{|s| + X}{d|e|}$$

- Additional term X is not dominant and is approximated by its expected value for a given $|s|$ and $|e|$

$$E_\ell = |\{\text{equations with } \ell \text{ errors}\}| \quad X = 2E_3 + 4E_5 + \dots$$

³Julia Chaulet. ‘Étude de cryptosystèmes à clé publique basés sur les codes MDPC quasi-cycliques’. PhD thesis. University Pierre et Marie Curie, Mar. 2017. URL:
<https://tel.archives-ouvertes.fr/tel-01599347>.

TRANSITIONS

Require: $H \in \{0, 1\}^{r \times n}$, $y \in \{0, 1\}^n$

while $(s \leftarrow yH^T) \neq 0$ **do**

$j \leftarrow \text{sample(context)}$

$T \leftarrow \text{threshold(context)}$

if $|s \cap h_j| \geq T$ **then**

$y_j \leftarrow 1 - y_j$

return y

- Thresholds defined by the algorithm
- Distributions known from [Cha17]⁴

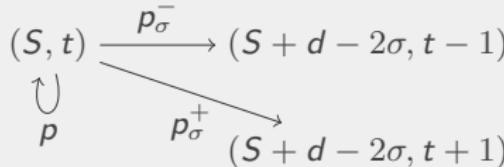
Transitions:



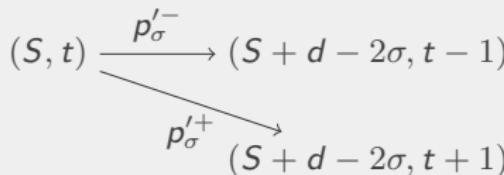
⁴Julia Chaulet. ‘Étude de cryptosystèmes à clé publique basés sur les codes MDPC quasi-cycliques’. PhD thesis. University Pierre et Marie Curie, Mar. 2017. URL:
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TRANSITIONS

■ Finite number of iterations



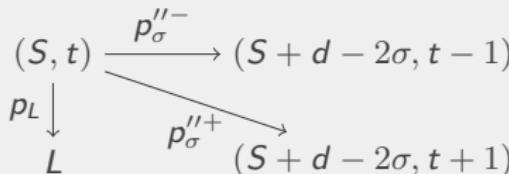
■ Infinite number of iterations



$$p'^{-} = \frac{p_\sigma^-}{1 - p}$$

$$p'^{+} = \frac{p_\sigma^+}{1 - p}$$

■ Infinite number of iterations considering the possibility of locking



$$p''^{-} = p'^{-}(1 - p_L)$$

$$p''^{+} = p'^{+}(1 - p_L)$$

For a fixed rate R :

- cost of an attack on the key:
 $\sim 2^{cw}$
- cost of an attack on the message:
 $\sim 2^{ct}$

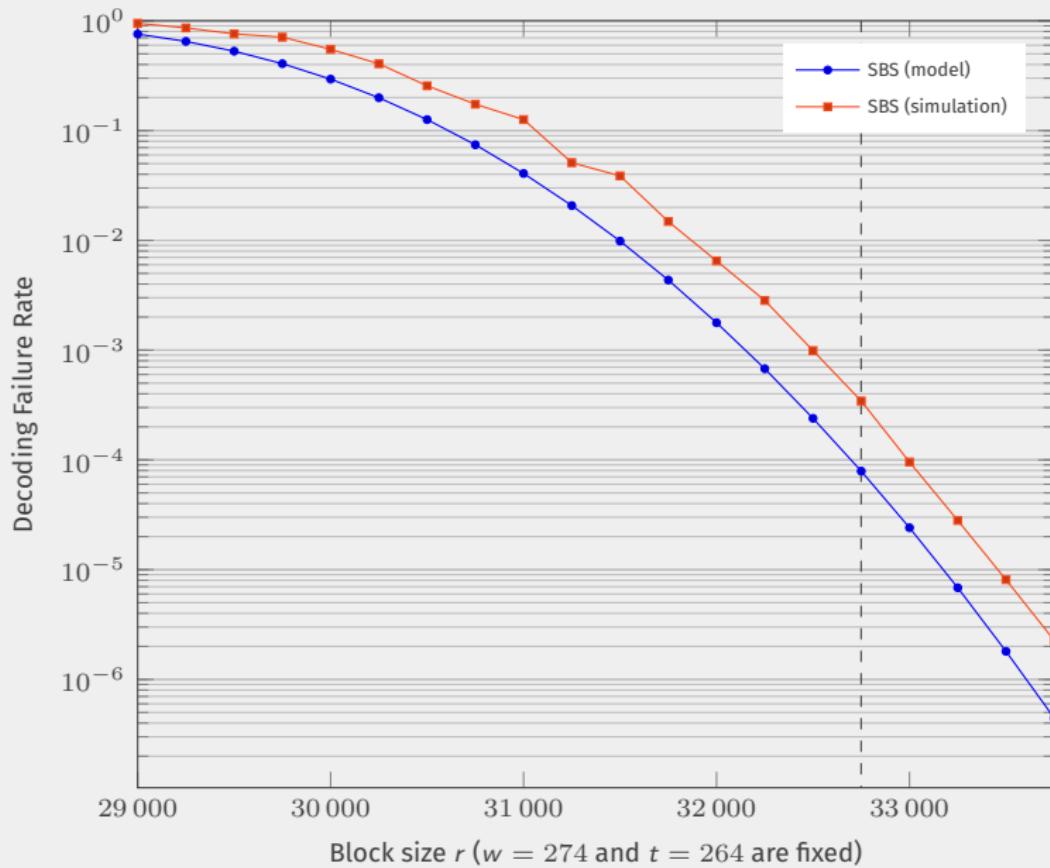
for some constant c

r : block size
 n : code length
 R : code rate
 w : row weight
 t : error weight

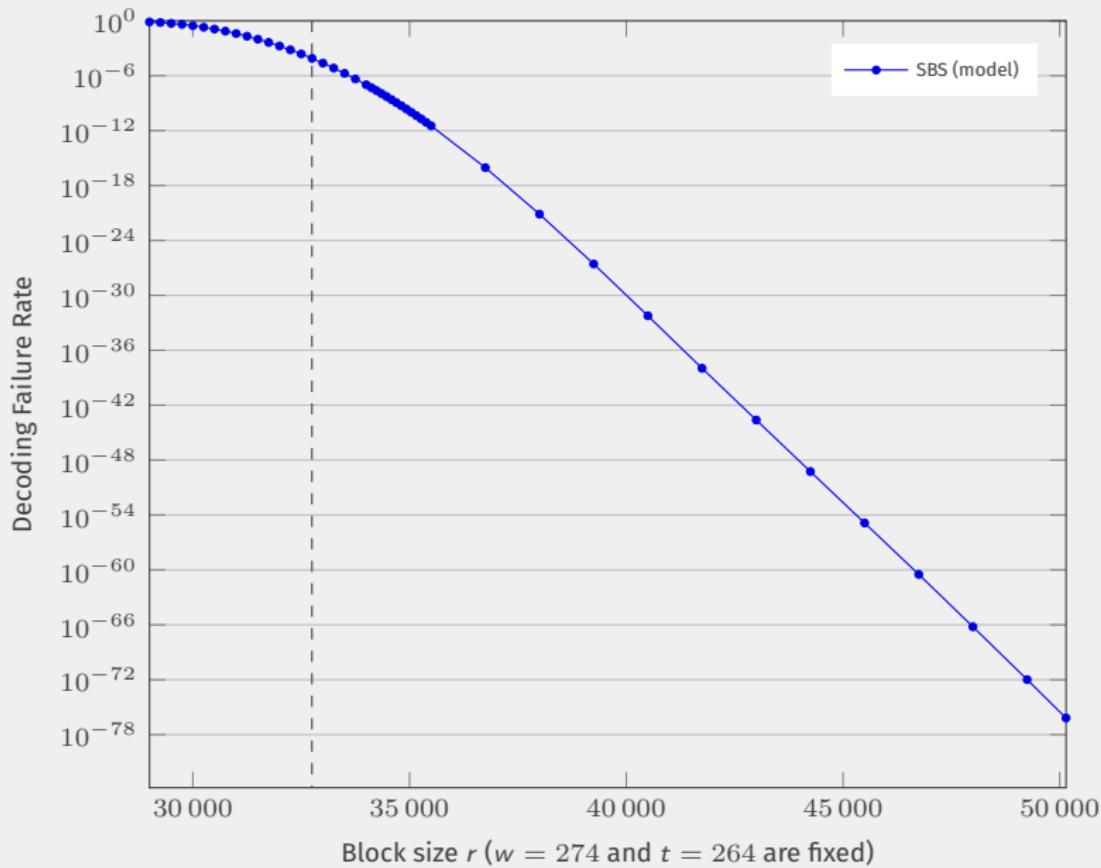
Changing r :

- same costs for these attacks
- different DFR

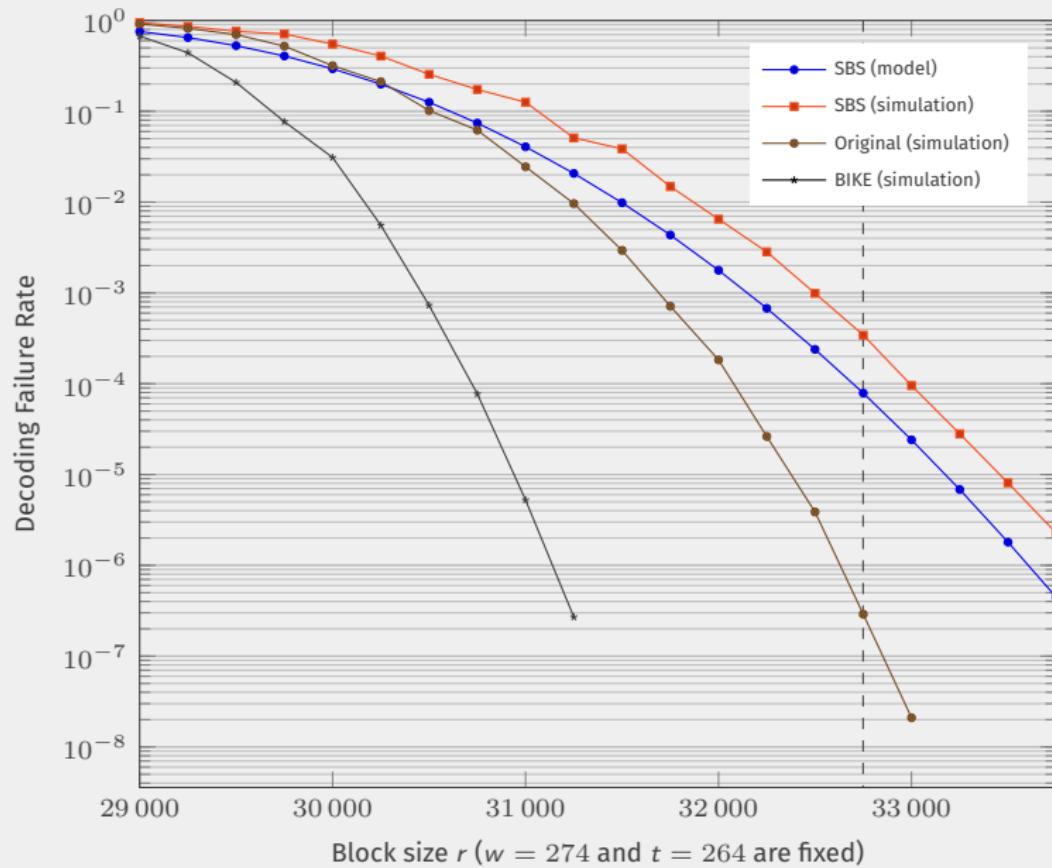
DFR OF THE STEP-BY-STEP ALGORITHM (∞ ITERATIONS)



DFR OF THE STEP-BY-STEP ALGORITHM (∞ ITERATIONS)



DFR OF OTHER ALGORITHMS



EXTRAPOLATING

	$r = 32749$	2^{-128}		2^{-256}		
	(a)	(b)	(c)	(d)	(e)	(f)
SBS (model)	-13.6		41 872		50 333	
SBS (simulation)	-11.5		40 952	48 610	45 772	66 020
Original	-21.7		36 950	39 766	39 837	48 215
BIKE	-47.5	-57.0	34 712	37 450	37 159	44 924

- (a): linearly extrapolated value for $\log_2(p_{\text{fail}}(32749))$;
- (b): quadratically extrapolated value for $\log_2(p_{\text{fail}}(32749))$;
- (c): minimal r such that $p_{\text{fail}}(r) < 2^{-128}$ assuming a quadratic evolution;
- (d): minimal r such that $p_{\text{fail}}(r) < 2^{-128}$ assuming a linear evolution;
- (e): minimal r such that $p_{\text{fail}}(r) < 2^{-256}$ assuming a quadratic evolution;
- (f): minimal r such that $p_{\text{fail}}(r) < 2^{-256}$ assuming a linear evolution.

CONCLUSION

- Defined a simpler decoding algorithm
 - Modeled this algorithm
 - Derived a theoretical DFR from that model
 - Assumed a similar behavior for other bitflipping algorithms
- Framework to estimate the DFR of other bitflipping algorithms for MDPC